

# Standard SMD LED PLCC-2



### **DESCRIPTION**

These new devices have been designed to meet the increasing demand for surface mounting technology.

This device is used for outdoor or for low power applications.

The package of the TLMD310. is the PLCC-2 (equivalent to a size B tantalum capacitor).

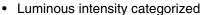
It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled up with clear epoxy.

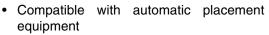
#### PRODUCT GROUP AND PACKAGE DATA

Product group: LED
Package: SMD PLCC-2
Product series: standard
Angle of half intensity: ± 60°

### **FEATURES**

• SMD LEDs with exceptional brightness







- · EIA and ICE standard package
- Compatible with infrared, vapor phase and wave solder processes according to CECC
- · Available in 8 mm tape
- · Low profile package
- Non-diffused lens: excellent for coupling to light pipes and backlighting
- · Low power consumption
- Luminous intensity ratio in one packaging unit  $I_{Vmax}/I_{Vmin} \le 2.0$ , optional  $\le 1.6$
- Lead (Pb)-free device

### **APPLICATIONS**

- Automotive: backlighting in dashboards and switches
- Telecommunication: indicator and backlighting in telephone and fax
- Indicator and backlight for audio and video equipment
- · Indicator and backlight for battery driven equipment
- · Small indicator for outdoor applications
- · Indicator and backlight in office equipment
- · Flat backlight for LCDs, switches and symbols
- · General use

PARTS TABLE		
PART	COLOR, LUMINOUS INTENSITY	TECHNOLOGY
TLMD3100	Red, I <sub>v</sub> > 20 mcd (typ.)	GaAlAs on GaAs
TLMD3101	Red, I <sub>V</sub> = (16 to 50) mcd	GaAIAs on GaAs
TLMD3105	Red, I <sub>V</sub> = (10 to 32) mcd	GaAlAs on GaAs



ABSOLUTE MAXIMUM RATINGS <sup>1)</sup> TLMD310.								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
Reverse voltage		$V_{R}$	6	V				
DC Forward current		I <sub>F</sub>	30	mA				
Surge forward current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	0.5	А				
Power dissipation	T <sub>amb</sub> ≤ 60 °C	P <sub>V</sub>	100	mW				
Junction temperature		T <sub>j</sub>	100	°C				
Operating temperature range		T <sub>amb</sub>	- 40 to + 100	°C				
Storage temperature range		T <sub>stg</sub>	- 55 to + 100	°C				
Soldering temperature	t ≤ 5 s	T <sub>sd</sub>	260	°C				
Thermal resistance junction/ ambient	mounted on PC board (pad size > 16 mm <sup>2</sup> )	R <sub>thJA</sub>	400	K/W				

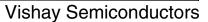
 $<sup>^{1)}</sup>$  T<sub>amb</sub> = 25 °C, unless otherwise specified

OPTICAL AND ELECTRICAL CHARACTERISTICS <sup>1)</sup> TLMD310., RED								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN	TYP.	MAX	UNIT	
Luminous intensity 3)	I <sub>F</sub> = 10 mA	TLMD3100	I <sub>V</sub>	10	20		mcd	
	IF = 10 IIIA	TLMD3101	I <sub>V</sub>	16		50	mcd	
Luminous intensity 2)	I <sub>F</sub> = 10 mA	TLMD3105	I <sub>V</sub>	10		32	mcd	
Luminous intensity	I <sub>F</sub> = 1 mA		I <sub>V</sub>		2		mcd	
Dominant wavelength	I <sub>F</sub> = 10 mA		$\lambda_{d}$		648		nm	
Peak wavelength	I <sub>F</sub> = 10 mA		$\lambda_{p}$		650		nm	
Angle of half intensity	I <sub>F</sub> = 10 mA		φ		± 60		deg	
Forward voltage	I <sub>F</sub> = 20 mA		V <sub>F</sub>		1.8	2.2	V	
Reverse voltage	I <sub>R</sub> = 10 μA		$V_{R}$	6	15		V	
Junction capacitance	V <sub>R</sub> = 0, f = 1 MHz		C <sub>j</sub>		15		pF	

## Note:

 $<sup>^{(1)}</sup>$  T<sub>amb</sub> = 25 °C, unless otherwise specified  $^{(2)}$  in one packing unit  $I_{Vmax}/I_{Vmin} \le 1.6$ 

 $<sup>^{3)}</sup>$  in one packing unit  $I_{Vmax}/I_{Vmin} \leq 2.0$ 





### **TYPICAL CHARACTERISTICS**

T<sub>amb</sub> = 25 °C, unless otherwise specified

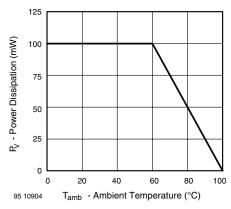


Figure 1. Power Dissipation vs. Ambient Temperature

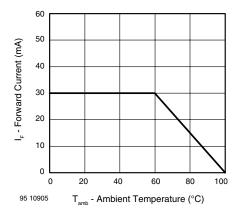


Figure 2. Forward Current vs. Ambient Temperature for InGaN

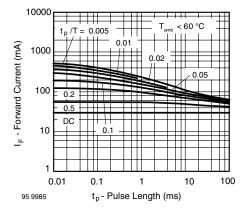


Figure 3. Pulse Forward Current vs. Pulse Duration

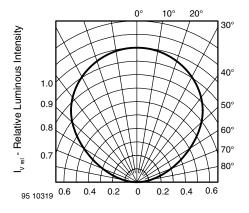


Figure 4. Rel. Luminous Intensity vs. Angular Displacement

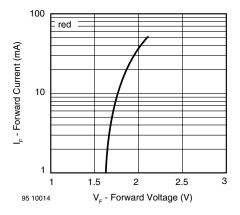


Figure 5.

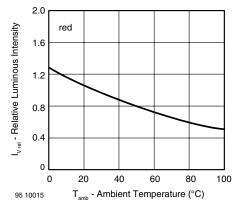


Figure 6. Rel. Luminous Intensity vs. Ambient Temperature



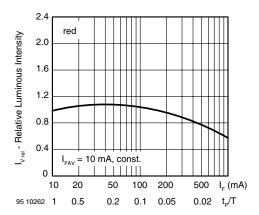


Figure 7. Rel. Lumin. Intensity vs. Forw. Current/Duty Cycle

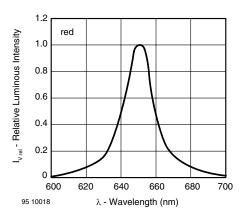


Figure 9. Relative Intensity vs. Wavelength

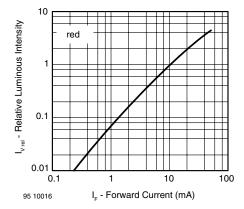
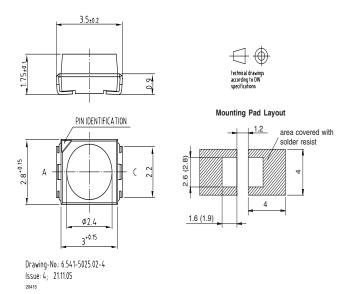


Figure 8. Relative Luminous Intensity vs. Forward Current

## **PACKAGE DIMENSIONS** in millimeters





## **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

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